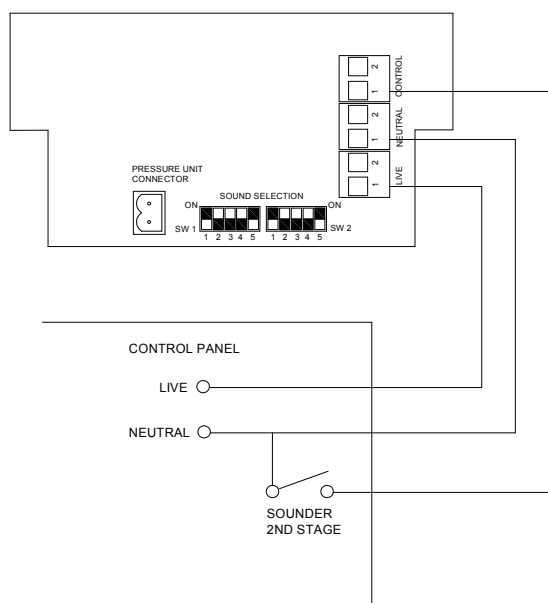




MODEL Y09

AC ALARM CONNECTIONS AND SOUND SIGNAL SELECTION



A. Two stage alarm by third core
 Line monitoring by supply reversal
 Select alarm stage 1 on SW1
 Select alarm stage 2 on SW2

A. Two stage alarm by third core
Line monitoring by supply reversal
Select alarm stage 1 on SW1
Select alarm stage 2 on SW2

B. Two stage alarm by supply reversal

Line monitoring by threshold method ($<1v$)

Select alarm stage 1 on SW1

Select alarm stage 2 on SW2

Strobe operated from separate supply

MODELS: - BREAK GLASS, PUSH BUTTON

The diagram shows a 7-bit shift register with bits numbered 1 to 7 from top to bottom. Bit 1 is labeled 'IN' and has an input line from the left. Bit 2 is labeled 'OUT' and has an output line to the left. Bit 3 is labeled 'IN' and has an input line from the left. Bit 4 is labeled 'IN' and has an input line from the left. Bit 5 is labeled 'IN' and has an input line from the left. Bit 6 is labeled 'IN' and has an input line from the left. Bit 7 is labeled 'IN' and has an input line from the left. A label 'END OF LINE DEVICE' is connected to the output line of bit 2.

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